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Claims searched: ALL

Examiner: Mr. Sat Satkunurath  
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**Patents Act 1977**  
**Search Report under Section 17**

**Databases searched:**

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.Q): H3R: RMC, RMX

Int Cl (Ed.6): H03D

Other: Online: WPI, JAPIO, EPODOC

**Documents considered to be relevant:**

Category	Identity of document and relevant passage		Relevant to claims
E, X	GB2332796 A	NOKIA - see especially figures 1, 5	6
Y	US 5552734	KIMURA - see especially figures 42, 45	1
Y	US 5355534	KIMURA - see especially figures 2, 3	1
Y	US 5113094	GRACE - see especially figure 3	1
X			6
Y	US 3983489	GITTINGER - see especially figures 1-4	1
X			6

- X Document indicating lack of novelty or inventive step  
Y Document indicating lack of inventive step if combined with one or more other documents of same category.  
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- A Document indicating technological background and/or state of the art.  
P Document published on or after the declared priority date but before the filing date of this invention.  
E Patent document published on or after, but with priority date earlier than, the filing date of this application.

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# UK Patent Application (19) GB (11) 2 332 796 A

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(56) Documents Cited

GB 2284116 A US 5113094 A US 4485488 A  
 US 3983489 A

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(54) Abstract Title

Mixer circuits

(57) A sub-harmonically pumped mixer has at least one anti-parallel diode pair 108, 110 switchable at twice the local oscillator frequency. The local oscillator is coupled to the diode pairs via an unbalanced to balanced transformer 112 which provides anti-phased signals to the diode pairs. In a second embodiment (fig.3 not shown), local oscillator signals in anti-phase are derived via a phase-splitter and applied to a circuit including two pairs of transistors of which two transistors conduct at a time to produce a local oscillator signal at double the frequency and mixed with an RF signal applied to the emitter electrodes connected in common. The mixer circuit may form part of a direct conversion or homodyne mixer for use in a portable communication device such as a radio telephone. The invention is particularly useful where control signals such as paging signals on the same frequency are received by all the telephones in the same cell.

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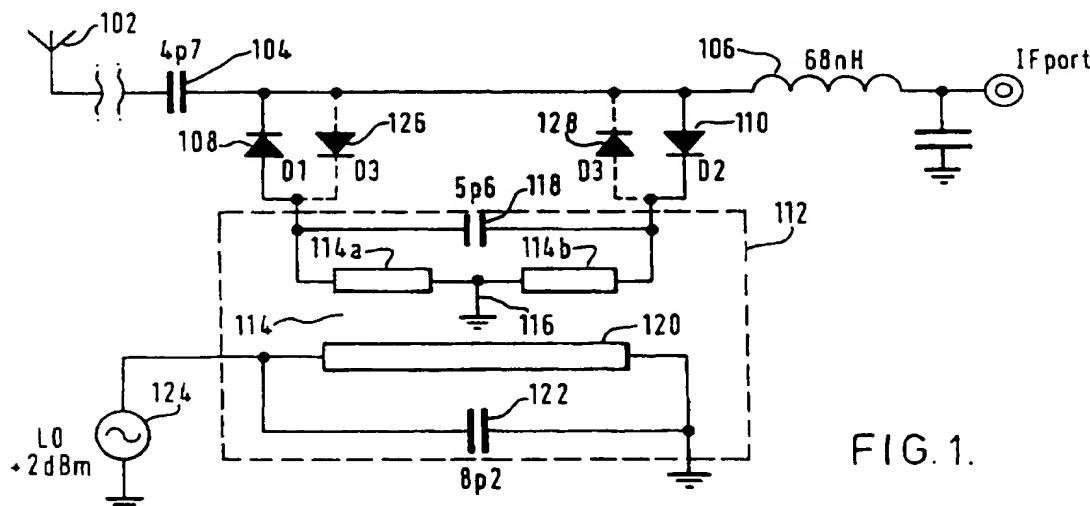
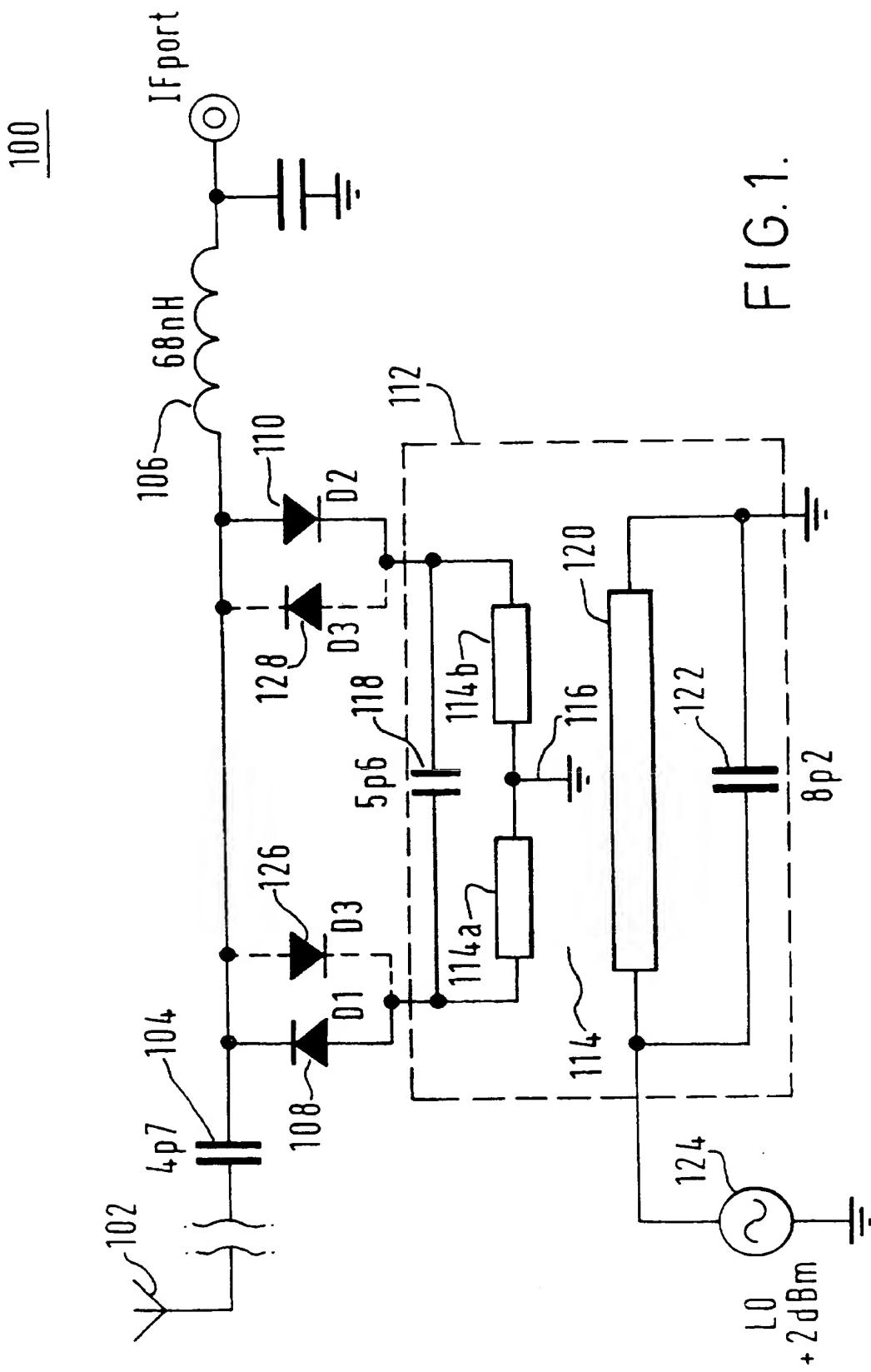


FIG. 1.

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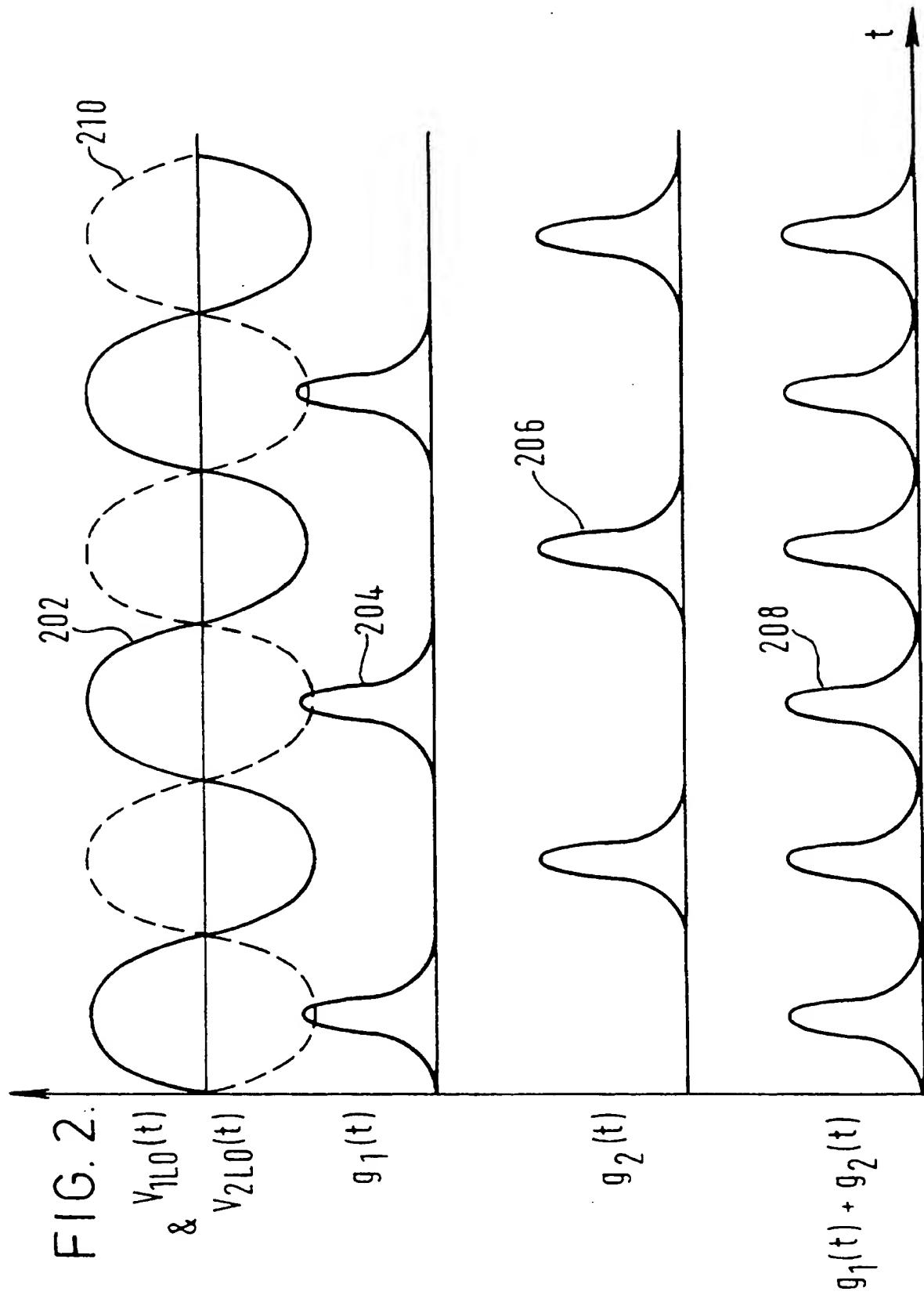


FIG. 2

$$\& v_{1L0}(t) \\ & v_{2L0}(t)$$

$$g_1(t)$$

$$g_2(t)$$

$$g_1(t) + g_2(t)$$

A MIXER FOR A RECEIVER

- 5 The present invention relates to a mixer for a receiver for a portable radio communication apparatus, and in particular to a mixer for a direct conversion receiver.

A general trend in portable communication apparatus is the reduction in  
10 volume, weight and power consumption of such apparatus. This has led to efforts towards reducing the number of elements and devices necessary to perform the functions associated with portable communication devices. In particular, the radio frequency front end of portable apparatus, which typically comprises a number of down-converting stages, is an area in which a  
15 reduction in the number of elements and devices would be beneficial.

One approach to reduce the number of stages in the radio frequency front-end is to convert a received radio frequency carrier signal down to a DC Intermediate Frequency (zero IF) in a single step. This is termed direct  
20 conversion and is carried out in receivers known by any one of the terms homodyne or zero IF receivers, as well as direct conversion receivers. To carry out direct conversion, a local oscillator signal (LO) having the same frequency as the radio frequency carrier signal (ie. the LO is "on-channel") is mixed with the carrier signal in a suitable non-linear device such as a mixer  
25 diode. The output of the mixer contains the sum and difference of the LO and the carrier signal. Thus a mixer product exists at twice the carrier signal, and also at DC (zero Hz). The high frequency mixer products can be filtered by a suitable low pass filter. Once the radio frequency carrier signal has been down-converted, the modulating signal may be de-modulated using an  
30 appropriate demodulator, e.g. an I/Q demodulator for an I/Q modulating signal.

In the field of radio telephony, particularly cellular telephony, use of a direct conversion receiver is not without certain drawbacks.

- 5 Radio telephones operating in the same cell of a cellular telephone network typically receive control signals on the same signal frequency as each other. This is necessary since such control signals are often so-called "broadcast" control signals such as paging signals which need to be received by all the telephones operating in the same cell. Signals received by radio telephones  
10 can be of very low power, e.g. below -100dBm and the sensitivity of the receivers is adapted accordingly.

Against the requirement for radio telephones in the same cell to be tuned to common frequencies, the introduction of direct conversion receivers with their  
15 strong on-board LO signals present the problem of interference. More specifically, spurious LO leakage from the receiver into the antenna can cause in-band interference with other nearby receivers tuned to the same channel. Therefore, such a receiver would require a very high level of isolation between the local oscillator and the antenna in order to avoid  
20 swamping or saturating receivers of any nearby radio telephones. Such high levels of isolation are very difficult to achieve due to stray capacitances directly coupling local oscillator energy into the antenna. This is the case even when conventional mixers such as a Gilbert cell is used.

- 25 Known methods addressing the problem of LO leakage include:
  - providing increased reverse-isolation in the RF path to the antenna, for example introducing multi-stage RF amplifiers,
  - inserting an isolator,
  - reducing the local oscillator drive power to very low levels.

This further improves isolation between the radio frequency and local oscillator ports.

In a second embodiment of the present invention, the rectifying means and  
5 the coupling means comprise a phase splitter and a plurality of transistors  
arranged in a network, wherein a signal output from the driver transistor  
drives the transistor network at a multiple of the local oscillator frequency.

The use of transistors in the second embodiment advantageously provides for  
10 the integration of the embodiment into an integrated circuit.

Preferably, the phase splitter provides anti-phase local oscillator drive signals  
to the plurality of transistors arranged in a network, and the driver transistor  
may comprise a unipolar transistor, conveniently an FET transistor, while the  
15 plurality of transistors arranged in a network may comprise bipolar transistors  
in a bridge configuration, or FET's

For this application of direct conversion receiver the local oscillator is half the  
received frequency.

20 The present invention extends to apparatus for a sub-harmonic mixer,  
comprising switching means, a first port for inputting a first frequency signal to  
the switching means, a transformer having an output electrically coupled to  
the switching means, and a second port for inputting a local oscillator signal to  
25 an input of the transformer, wherein a signal output from the transformer  
drives the switching means at an even multiple of the local oscillator  
frequency for down-converting the first signal frequency.

30 In a further aspect of the invention there is provided a method for sub-  
harmonically mixing a signal, comprising receiving a first frequency signal,

generating a local oscillator signal and switching the first frequency signal at an even multiple of the local oscillator signal, for down-converting the first frequency signal.

- 5 An advantageous feature of the mixers disclosed herein is that they are bi-directional.

Specific embodiments in accordance with the invention are hereinafter described, by way of example only, and with reference to the accompanying  
10 drawings, in which:

Figure 1 schematically represents a first embodiment of a sub-harmonic mixer in accordance with the present invention;

- 15 Figure 2 schematically shows the correspondence of the local oscillator voltage across diodes D1 and D2, and the respective and combined conductance waveforms for D1 and D2 for the embodiment described with reference to

Figure 1;

20

Figure 3 schematically represents a second embodiment of a sub-harmonic mixer in accordance with the present invention; and

- 25 Figure 4 schematically shows the voltage waveforms of the local oscillator and IF ports of the second embodiment.

Figures 1 and 3 show radio telephones 100 including sub-harmonic mixers in accordance with first and second embodiments of the invention respectively. The sub-harmonic mixers are adapted to be operable within a 840MHz to  
30 1080MHz frequency range. The radio telephones 100 include all the usual

functional elements associated with radio telephones, such as a key pad, display, microphone, ear-piece, transceiver unit and a micro-controller to control all the functional elements.

- 5 Referring to Figure 1, radio telephone 100 includes an antenna 102, typically capable of receiving and transmitting radio frequency signals. Antenna 102 is coupled to a transceiver comprising a transmitter and receiver, but the sub-harmonic mixer forms a part of a receiver .
- 10 A 4.7 picofarad capacitor 104 decouples the mixer from the input for the radio frequency carrier signal. Decoupling capacitor 104 is series coupled to a 68nH inductor 106, which acts as a low-pass output filter. An anti-parallel diode pair comprising Schottky diodes D1 and D2, references 108 and 110 respectively, is parallel coupled between capacitor 104 and inductor 106. The 15 anti-parallel pair D1, D2 is also coupled to the balanced output of balanced-to-unbalanced (Balun) transition 112.

- Balun 112 comprises a balanced stripline output 114 having respective subsections 114a and 114b centred about an earth reference potential 116. A 20 5.6 picofarad tuning capacitor 118 is coupled across balanced output 114. Transformer Balun 112 further comprises unbalanced stripline input 120, having an 8.2 picofarad tuning capacitor 122 coupled across it. A local oscillator signal LO is coupled to the unbalanced input from local oscillator generator 124. A typical LO output power level is 2dBm. The balanced 25 output 114 produces anti-phased signals in respective arms 114a and 114b.

- Local oscillator generator 124 supplies an LO signal to the unbalanced input 120 of Balun 112. The LO signal is coupled to balanced output 114. A voltage  $V_{1LO}(t)$  is applied across the terminals of D1 (108), and a voltage 30  $V_{2LO}(t)$  is applied across the terminals of D2 (110). Voltage  $V_{1LO}(t)$  is shown

schematically in Figure 2 and is labelled reference 202, and  $V_{2\text{Lo}}(t)$  is reference 210 (dotted). The conductance waveforms  $g_1(t)$ ,  $g_2(t)$  for D1 (108) and D2 (110) are shown labelled 204/206 respectively in Figure 2.

- 5 The combined conductance waveform  $g_1(t) + g_2(t)$  is shown labelled 208 in Figure 2. As can be seen from Figure 2, the combined conductance waveform 208 has a frequency which is twice that of the local oscillator signals 202 and 210 respectively produced at arms 114a and 114b.
- 10 Referring back to Figure 1, the conductance waveform 208 of anti-parallel diode pair D1/D2 (108, 110) causes switching of the radio frequency carrier signal input across capacitor 104 at a rate twice that of the LO signal. This is achieved by coupling the radio frequency carrier signal to ground in accordance with waveform 208. Switching the radio frequency signal in this manner down converts it to a frequency given by the difference in frequency between the radio frequency signal  $f_{RF}$ , and twice the local oscillator frequency  $2f_{\text{Lo}}$ . It is apparent to a person of ordinary skill in the art that the fundamental response also includes a signal at the sum ( $f_{RF} + 2f_{\text{Lo}}$ ) of the frequencies. The sum and other higher order products are not relevant to the described embodiment, and are attenuated by low-pass filter 106, and capacitor.
- 15
- 20

By selecting the LO frequency,  $f_{\text{Lo}}$ , to be half the radio frequency,  $f_{RF}$ , direct conversion is possible. For the embodiment of Figure 1, the LO frequency would be in the range 420MHz to 540MHz. Since the LO pump fundamental is half  $f_{RF}$ , there is very little 2xLO signal generated directly from the mixing process (at  $f_{RF}$ ) (i.e. 2nd harmonic frequency of the LO frequency). Due to the second harmonic's naturally low power level and the selective filtering inherent in the VCO, the second harmonic is at a much lower power level than the fundamental frequency. Additionally, since the fundamental frequency is not multiplied and the harmonic (i.e. twice LO frequency) component is

filtered, the phase noise remains substantially constant. Furthermore, a high phase balance between respective anti-phased arms 114a and 114b reduces generation of signals at the second harmonic of the LO.

- 5      Balun 112 need not be stripline, but may be another Balun transition. However, it is important that the respective signals produced at 114a and 114b are very well phase-balanced. Discrete balluns lend themselves to high levels of phase balance, and are the preferred embodiment. In general, the circuit components for the sub-harmonic pumped mixer shown in Figure 1 are
- 10     chosen and configured to give a high level of phase and amplitude balance across the arms of the anti-diode parallel pair. This optimises the isolation from antenna 102. For example, diodes D1/D2 (108, 110) are fabricated from the same area of wafer during their manufacture.
- 15     Turning to Figure 3, there is shown a schematic circuit for the second embodiment of a sub-harmonic mixer of the present invention forming part of a receiver of a radio telephone 100. The radio telephone 100 includes an antenna 302, coupled to a transceiver and capable of receiving and transmitting radio frequency signals.
- 20     In the receiver, the antenna 302 is connected at an input of a transistor network 303 comprising four transistors 304, 305, 306, 307, and an intermediate frequency (IF) port 308 is connected at an output of the network 303.
- 25     The transistors 304, 305, 306, 307 are bipolar transistors arranged generally in a bridge configuration; transistors 304 and 307 are NPN transistors, transistors 305 and 306 are PNP transistors.

The antenna 302 and IF port 308 are coupled to the transistor network 303 between the emitter of transistor 304 and the collector of transistor 305, and between the emitter of transistor 306 and the collector of transistor 307. A constant DC voltage supply is connected to the respective collectors of  
5 transistors 304 and 306, and respective emitters of transistors 305 and 307 are connected to ground. The respective bases of the transistors are connected to a phase splitter circuit 309.

10 The 0 degrees phase shifted LO signal is connected to the bases of transistors 304 and 306, and the 180 degrees phase shifted LO signal is connected to the bases of the transistors 305 and 307.

The operation of the circuit will now be described.

15 Local oscillator generator 310 operating at half that required for mixing supplies an LO signal to the phase splitter 309. Phase splitter 309 generates two signals, one in-phase another in anti-phase local oscillator drive signals to transistors 304, 306, and 305 307 respectively. During the in-phase LO signal on its positive half cycle is applied to transistors 304 and 306, transistor 304  
20 conducts and transistor 306 is switched off. During the in-phase LO signal on its negative half cycle is applied to transistors 304 and 306, transistor 304 is switched off and transistor 306 conducts. During the anti-phase LO signal is on its positive half cycle, transistor 307 conducts and transistor 305 is off. During the anti-phase LO signal is on its negative half cycle, transistor 307 is  
25 off and transistor 305 conducts. Since the in phase and anti-phase signals are 180 degrees phase shifted on the positive half-cycle of the local oscillator drive, transistors 304 and transistors 305 are on. On the negative half-cycle of the local oscillator drive, transistors 306 and transistors 307 are on. In either case, the antenna and IF port is connected to a virtual earth when a pair of  
30 transistors conduct. Since there are two pairs of transistors driven on opposite

half-cycles, the summing point is driven close to ground on both half-cycles hence the mixer switches in the same sense at twice the local oscillator frequency.

- 5     Figure 4 illustrates the conductance of the transistor network in relation to the phase of the LO. It shows two opposite phase signals and the conductance of A the in-phase LO applied to transistors 304 and 306, and B the LO applied antiphase to transistors 305 and 307. A' is the current through the 304 and 305 transistor pair. B' is the current through 306 and 307 transistor pair. It  
10    shows that during every half cycle of the applied LO the subharmonic mixer switches. See Figure 3 for correspondence of A A' and B B'.

- In view of the foregoing description it will be evident to a person skilled in the art that various modifications may be made within the scope of the invention.  
15    For example, dual anti-parallel diode pairs may be utilised, further comprising diodes D3/D4 (126, 128) and shown dotted in Figure 1.. This further improves phase and amplitude balance, thereby further improving isolation from antenna 102. Additionally, the invention is not limited to down-converting carried signal frequencies for control signals, but is applicable to down-  
20    converting in general.

- The scope of the present disclosure includes any novel feature or combination of features disclosed therein either explicitly or implicitly or any generalisation thereof irrespective of whether or not it relates to the claimed  
25    invention or mitigates any or all of the problems addressed by the present invention. The applicant hereby gives notice that new claims may be formulated to such features during prosecution of this application or of any such further application derived therefrom.

CLAIMS

1. A receiver for a portable radio communication apparatus comprising a first port for inputting a received frequency signal and a second port for  
5 inputting a local oscillator signal, means for rectifying the input local oscillator signal to provide a conductance waveform at a multiple of the local oscillator signal and means for mixing the received frequency signal with the conductance waveform at said multiple of the local oscillator signal frequency for down-converting the received frequency signal to an intermediate  
10 frequency signal.
2. A receiver according to claim 1, wherein the rectifying means and the coupling means comprise a phase splitter and switching means, wherein a signal output from the phase splitter drives the switching means at a multiple  
15 of the local oscillator frequency.
3. A receiver according to claim 2, wherein the phase splitter comprises a balanced anti-phase output transformer.  
20 4. A receiver according to claim 3, wherein the switching means comprises an anti-parallel diode pair, and respective diodes are electrically coupled to respective arms of the balanced output of the transformer.
- 25 5. A receiver according to claim 4, wherein the switching means comprises a further anti-parallel diode pair, and respective diodes of the further anti-parallel pair are reversibly electrically coupled to respective arms of the balanced output of the transformer relative to the said anti-parallel diode pair.

6. A receiver according to claim 4 or claim 5, wherein the diodes are Schottky diodes.
7. A receiver according to any of claims 4 to 6, wherein the diodes are  
5 fabricated from the same region of semi-conductor wafer during manufacture.
8. A receiver according to claim 2 or any claim dependent thereon,  
wherein the transformer is a discrete element.
- 10 9. Apparatus according to claim 2 or any claim dependent thereon,  
wherein the phase splitter is a strip line, slot-line, co-planar microstrip and/or  
microstrip balanced to unbalanced transformer.
10. A receiver according to claim 1, wherein the rectifying means and the  
15 coupling means comprise a phase splitter and a plurality of transistors  
arranged in a network, wherein a signal output from the driver transistor  
drives the transistor network at a multiple of the local oscillator frequency.
11. A receiver according to claim 10, wherein the phase splitter provides  
20 anti-phase local oscillator drive signals to the plurality of transistors arranged  
in a network.
12. Apparatus according to any claim for direct conversion receivers,  
wherein the second frequency is half the first frequency.  
25
13. Apparatus for a sub-harmonically pumped mixer, comprising:  
switching means;  
a first port for inputting a first frequency signal to the switching means;  
a transformer having an output electrically coupled to the switching  
30 means; and

a second port for inputting a local oscillator signal to an input of the transformer, wherein a signal output from the transformer drives the switching means at an even multiple of the local oscillator frequency for down converting the first signal frequency.

5

14. A method for sub-harmonically mixing a signal, comprising:  
receiving a first frequency signal;  
generating a local oscillator signal

10 and switching the first frequency signal at an even multiple of the local oscillator signal, for down-converting the first frequency signal.

15. Apparatus substantially as hereinbefore described, and with reference to the drawings.

15

16. A method substantially as hereinbefore described, and with reference to the drawings.

17. A radio telephone comprising a receiver and/or operable in accordance  
20 with any preceding claim.



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**Patents Act 1977**  
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Other:

**Documents considered to be relevant:**

Category	Identity of document and relevant passage		Relevant to claims
X	GB 2284116 A	NEC - see especially figures 6-8, 10, 11, 13-28	14
X	US 5113094	GRACE - see especially figure 3	1-3, 13 and 14
X	US 4485488	HOUDART - see especially figures 4-7	14
X	US 3983489	GITTINGER - see especially figures 3, 4	1-5, 8, 13 and 14

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Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.